Demo photo only. Actual product outlook and marking may vary.

UVLO	**OCP**	**OVP**	**OTP**
Metal Case	**ON/OFF Remote**	**PI Filter Built-in**	**2000VDC Isolation**

Features

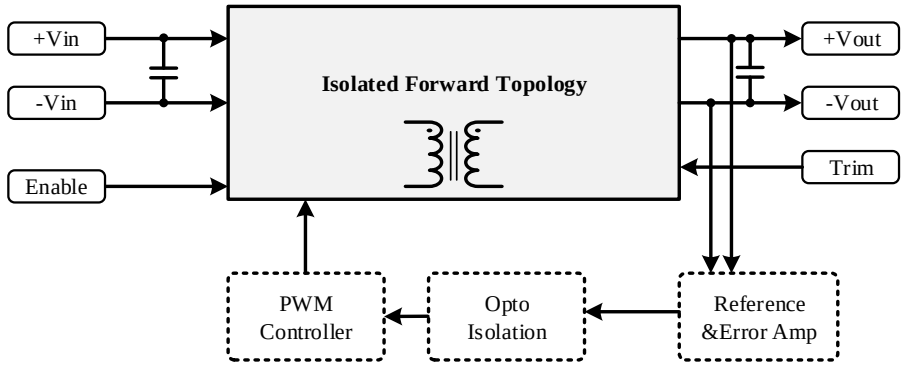
- High efficiency – **91%@ 5V /8A** Full Load
- **2:1 / 4:1 Ultra-Wide** input range
- Compact size – **2.0" x 1.0" x 0.4"**
- -40˚C to +60˚C operation without derating
- Fixed switching frequency provides predictable EMI
- Output current limit and short circuit protection
- No life-span constrained Capacitor inside
- **MTBF≥1.6M hours** @50˚C GB (BellCoreTR-332)

Applications

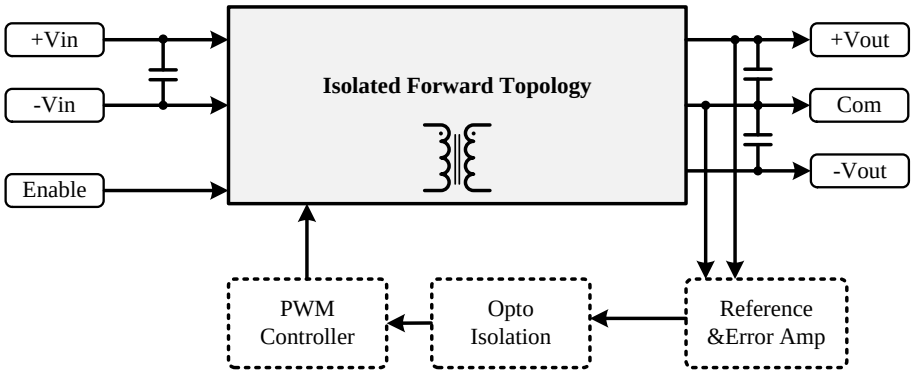
- Railway System
- Wireless Network
- Telecom / Datacom
- Industry Control System
- Distributed Power Architectures
- Semiconductor Equipment

Description

Evolving Sirius-Chivalry series **New** generation converter is composed of Isolated, board-mountable, fixed switching frequency dc-dc converters that use synchronous rectification to achieve extremely high power conversion efficiency. These DC-DC converter modules use advanced power processing, control and packaging technologies to enhance the performance, flexibility, reliability and cost effectiveness of mature power components. Each module is supplied completely encased to provide protection from the harsh environments seen in many industrial and transportation applications.



ESCN Single Series Block Diagram



ESCN Dual Series Block Diagram

MODEL NUMBER STRUCTURE

ESCN	018	033	-	S	-	P	-	F	40V0

Series Name	Input Voltage (VDC)	Output Voltage (VDC)	Output Quantity	Remote Control Option	Shape	Watt
Evolving **S**irius-**C**hivalry series – **N**ew generation	**012**： 9-18 **018**： 9-36 **024**：18-36 **036**：18-75 **048**：36-75	**033**：3.3 **050**： 5 **120**：12 **150**：15 **240**：24	**S**: Single	**P**: Positive logic **N**: Negative logic	**F**：Flat+FR4 **CS**: Full Metal (6-Side) **P:** Heat Sink **R:** Base Plate	**20** **30** **40** **50** **60**
	110: 40-160	**120**：±12 **150**：±15 **240**：±24	**D**: Dual			

Model Selection Guide

Typical @ Ta=+25 ℃ under nominal line voltage conditions unless noted.

Model	Input			Output			Efficiency
	Voltage(V)		Current (A)	Voltage	Current	Power	
	Range	Nominal	Full load	(V)	(A)	(W)	Typ.(%)
ESCN024033-S-P-F30V0	18-36	24	1.40	3.3	9.1	30	89%
ESCN024050-S-P-F30V0	18-36	24	1.39	5	6.0	30	90%
ESCN024120-S-P-F30V0	18-36	24	1.40	12	2.5	30	89%
ESCN024150-S-P-F30V0	18-36	24	1.40	15	2.0	30	89%
ESCN024120-D-P-F30V0	18-36	24	1.40	±12	±1.3	30	89%
ESCN024150-D-P-F30V0	18-36	24	1.40	±15	±1.0	30	89%
ESCN024033-S-P-F40V0	18-36	24	1.87	3.3	12.1	40	89%
ESCN024050-S-P-F40V0	18-36	24	1.85	5	8.0	40	90%
ESCN024120-S-P-F40V0	18-36	24	1.87	12	3.3	40	89%
ESCN024150-S-P-F40V0	18-36	24	1.87	15	2.7	40	89%
ESCN024240-S-P-F40V0	18-36	24	1.87	24	1.66	40	89%
ESCN024120-D-P-F40V0	18-36	24	1.87	±12	±1.7	40	89%
ESCN024150-D-P-F40V0	18-36	24	1.87	±15	±1.3	40	89%
ESCN048033-S-P-F30V0	36-75	48	0.70	3.3	9.1	30	89%
ESCN048050-S-P-F30V0	36-75	48	0.69	5	6.0	30	90%
ESCN048120-S-P-F30V0	36-75	48	0.70	12	2.5	30	89%
ESCN048150-S-P-F30V0	36-75	48	0.70	15	2.0	30	89%
ESCN048120-D-P-F30V0	36-75	48	0.70	±12	±1.3	30	89%
ESCN048150-D-P-F30V0	36-75	48	0.70	±15	±1.0	30	89%
ESCN048033-S-P-F40V0	36-75	48	0.94	3.3	12.1	40	89%
ESCN048050-S-P-F40V0	36-75	48	0.93	5	8.0	40	90%
ESCN048120-S-P-F40V0	36-75	48	0.94	12	3.3	40	89%
ESCN048150-S-P-F40V0	36-75	48	0.94	15	2.7	40	89%
ESCN048120-D-P-F40V0	36-75	48	0.94	±12	±1.7	40	89%
ESCN048150-D-P-F40V0	36-75	48	0.94	±15	±1.3	40	89%

※ Modification or customized design is available. Please contact us for detail.

Model Selection Guide Typical @ Ta=+25 ℃ under nominal line voltage conditions unless noted.

Model	Input			Output			Efficiency
	Voltage(V)		Current (A)	Voltage	Current	Power	
	Range	Nominal	Full load	(V)	(A)	(W)	Typ.(%)
ESCN018033-S-P-F30V0	9-36	18	1.87	3.3	9.1	30	89%
ESCN018050-S-P-F30V0	9-36	18	1.85	5	6.0	30	90%
ESCN018120-S-P-F30V0	9-36	18	1.87	12	2.5	30	89%
ESCN018150-S-P-F30V0	9-36	18	1.87	15	2.0	30	89%
ESCN018120-D-P-F30V0	9-36	18	1.87	±12	±1.3	30	89%
ESCN018150-D-P-F30V0	9-36	18	1.87	±15	±1.0	30	89%
ESCN018033-S-P-F40V0	9-36	18	2.50	3.3	12.1	40	89%
ESCN018050-S-P-F40V0	9-36	18	2.47	5	8.0	40	90%
ESCN018120-S-P-F40V0	9-36	18	2.50	12	3.3	40	89%
ESCN018150-S-P-F40V0	9-36	18	2.50	15	2.7	40	89%
ESCN018120-D-P-F40V0	9-36	18	2.50	±12	±1.7	40	89%
ESCN018150-D-P-F40V0	9-36	18	2.50	±15	±1.3	40	89%
ESCN018050-S-P-P50V0	9-36	18	3.09	5	10	50	90%
ESCN018120-S-P-P50V0	9-36	18	3.12	12	4.16	50	89%
ESCN018240-S-P-P50V0	9-36	18	3.12	24	2.08	50	89%
ESCN036033-S-P-F30V0	18-75	36	0.94	3.3	9.1	30	89%
ESCN036050-S-P-F30V0	18-75	36	0.93	5	6.0	30	90%
ESCN036120-S-P-F30V0	18-75	36	0.94	12	2.5	30	89%
ESCN036150-S-P-F30V0	18-75	36	0.94	15	2.0	30	89%
ESCN036120-D-P-F30V0	18-75	36	0.94	±12	±1.3	30	89%
ESCN036150-D-P-F30V0	18-75	36	0.94	±15	±1.0	30	89%
ESCN036033-S-P-F40V0	18-75	36	1.25	3.3	12.1	40	89%
ESCN036050-S-P-F40V0	18-75	36	1.23	5	8.0	40	90%
ESCN036120-S-P-F40V0	18-75	36	1.25	12	3.3	40	89%
ESCN036150-S-P-F40V0	18-75	36	1.25	15	2.7	40	89%
ESCN036120-D-P-F40V0	18-75	36	1.25	±12	±1.7	40	89%
ESCN036150-D-P-F40V0	18-75	36	1.25	±15	±1.3	40	89%
ESCN036050-S-P-P50V0	18-75	36	1.54	5	10	50	90%
ESCN036120-S-P-P50V0	18-75	36	1.56	12	4.16	50	89%
ESCN036240-S-P-P50V0	18-75	36	1.56	24	2.08	50	89%
ESCN110033-S-P-F30V0	40-160	110	0.31	3.3	9.1	30	88%
ESCN110050-S-P-F30V0	40-160	110	0.31	5	6.0	30	89%
ESCN110120-S-P-F30V0	40-160	110	0.31	12	2.5	30	88%
ESCN110150-S-P-F30V0	40-160	110	0.31	15	2.0	30	88%
ESCN110240-S-P-F30V0	40-160	110	0.31	24	0.8	30	88%
ESCN110120-D-P-F30V0	40-160	110	0.31	±12	±1.3	30	88%
ESCN110150-D-P-F30V0	40-160	110	0.31	±15	±1.0	30	88%
ESCN110033-S-P-F40V0	40-160	110	0.41	3.3	12.1	40	88%
ESCN110050-S-P-F40V0	40-160	110	0.41	5	8.0	40	89%
ESCN110120-S-P-F40V0	40-160	110	0.41	12	3.3	40	88%
ESCN110150-S-P-F40V0	40-160	110	0.41	15	2.7	40	88%
ESCN110120-D-P-F40V0	40-160	110	0.41	±12	±1.6	40	88%
ESCN110150-D-P-F40V0	40-160	110	0.41	±15	±1.3	40	88%

※ Modification or customized design is available. Please contact us for detail.

Electrical Specifications

Input Specifications

(Typical @ Ta=+25°C under nominal line voltage conditions unless noted.)

Parameter	Notes and Conditions		Min.	Typ	Max.	Unit
Transient Input Voltage Ranges	ESCN012&18&24 Models (100ms max)				50	VDC
	ESCN036&48 Models (100ms max)				80	
	ESCN110 Models (100ms max)				180	
Operating Input Voltage Ranges	ESC012&18 Models		9	18	36	VDC
	ESC024 Models		18	24	36	
	ESC036 Models		18	36	75	
	ESC048 Models		36	48	75	
	ESC110 Models		40	110	160	
Under-Voltage Lockout Start Up Voltage	ESC012&18 Models			8.5	9	VDC
	ESC024 Models			17.5	18	
	ESC036 Models			17.5	18	
	ESC048 Models			35	36	
	ESC110 Models			38	40	
Under-Voltage Lockout Shutdown Voltage	ESC012&18 Models		7	8		VDC
	ESC024 Models		16	17		
	ESC036 Models		16	17		
	ESC048 Models		32	34		
	ESC110 Models		35	37		
Enable Function Input	Positive Logic	ON	Open or 8 ~ 20			VDC
		OFF	Short or 0 ~ 1.2			
	Negative Logic	ON	Short or 0 ~ 1.2			VDC
		OFF	Open or 8 ~ 20			
Input Filter	All Models		Built-in PI Filter			

Output Specifications

Parameter	Notes and Conditions		Min.	Typ	Max.	Unit
Output Voltage Accuracy	V_{NOM} 50% Load				±1.5	%
Line Regulation	Low line to High line				±0.3	%
Load Regulation	10% to 100% Load				±0.5	%
Minimum Load	Single Output		0			%
	Dual Output		10			%
Output Ripple & Noise Voltage	Bandwidth 20MHz and With 1uFMLCC.Output Capacitor Each Output	3.3V&5V			100	mVp-p
		All others		1	1.5	$\%V_{pk-pk}$
Temperature Drift					±0.04	% /°C
Transient Recovery Time	25% load step change			800		µSec.
Transient Peak Deviation	△Io/△t=2.5A/us				±2	%Vo
Start-Up Time	When use Enable Function			20		mSec.
Trimming Output Voltage	V_{NOM} 10% Load			±10		%
Over Voltage Protection	V_{NOM} 10% Load			120		%
Output Power Protection	V_{NOM} (Current limit / Hiccup Mode)			120		%

General Specifications

Parameter	Notes and Conditions	Min.	Typ	Max.	Unit
Switching Frequency	V_{NOM}	220		330	KHz
Storage Temperature Range	All Models	-60		125	˚C
Operating Case Temperature	All Models	-45		115	˚C
Over Temperature Protection	All Models, Auto. Recovery		120		˚C
Thermal Impedance	Natural convection (Metal Case –Flat)	10(Vertical)			˚C/Watt
		13(horizontal)			
Isolation Voltage Input to Output	All models, 1 Minute	2000			VDC
Isolation Resistance Input to Output	All models, 500VDC,At 70%RH	100			MΩ
Isolation Capacitance Input to Output	All models		1500		pF
Humidity (Non Condensing)	All models			95	%
Calculated MTBF	BellCore-TR-332@ 50˚C G.B		1.6		M HR
Thermal Shock	Environmental Engineering Experimental Tests	MIL-STD-810F			
Vibration		MIL-STD-810F			
Drop		MIL-STD-810F			
Weight	Shape-F (Flat)	31(1.09)			g (oz.)
	Shape-P (With Heat Sink On Top)	39(1.38)			
	Shape-CS (6-Shaped Metal Case)	30(1.06)			
	Shape-R (Base Plate)	41(1.45)			
Dimensions	Shape-F (Flat)	2.0" x 1.0" x 0.4" (50.8 x 25.4 x 10.4mm)			
	Shape-P (With Heat Sink On Top)	2.0" x 1.20" x 0.5" (50.8 x 30.5 x 13.2mm)			
	Shape-CS (6-Shaped Metal Case)	2.0" x 1.20" x 0.4" (50.8 x 25.4 x 10.4mm)			
	Shape-R (Base Plate)	2.0" x 1.60" x 0.45" (50.8 x 40.64 x 11.4mm)			
Case Material	Shape-F (Flat)	Aluminum + FR4 (Non-Conductive Base)			
	Shape-P (With Heat Sink On Top)				
	Shape-CS (6-Shaped Metal Case)	Aluminum			
	Shape-R (Base Plate)				
Potting Material		Silicone			

Standards Compliance

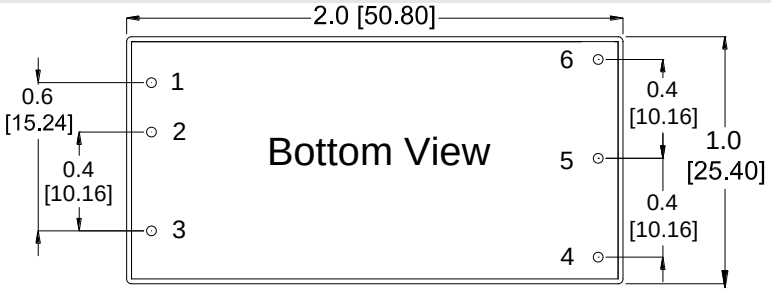
Parameter	Standard	Test Conditions	Performance Criteria
Environmental Compliance	Reach; RoHS		PASS
EMI	EN55022		Class A / Class B
ESD	EN61000-4-2	±4 kV Air Discharge ±4 kV Contact Discharge	Crit. A
Radiated Immunity	EN61000-4-3	Level 2, 3 V/m	Crit. A
Fast Transient	EN61000-4-4	±2 kV Applied	Crit. A
Surge	EN61000-4-5	±2 kV Applied	Crit. A
Conducted Immunity	EN61000-4-6	Level 2, 3 V rms	Crit. A

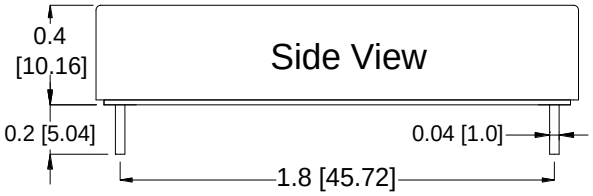
The standard modules meet EN55022 Class A. Then Class B standard with external components.

It is recommended to protect the input by fuses or other protection devices.

Mechanical Drawing & Pin Assignments

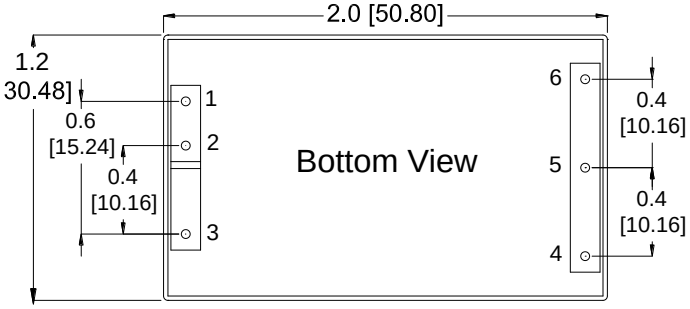
Suffix –F

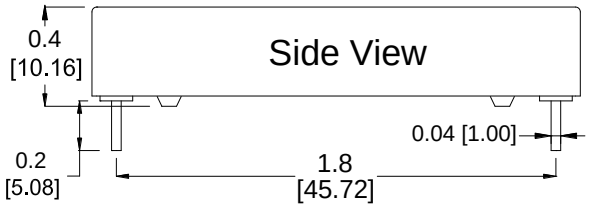




(Metal Case –Flat) "Non-Conductive Base"

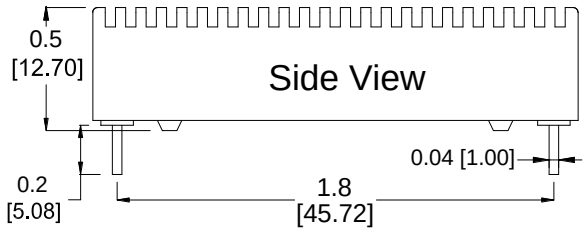
Suffix –CS



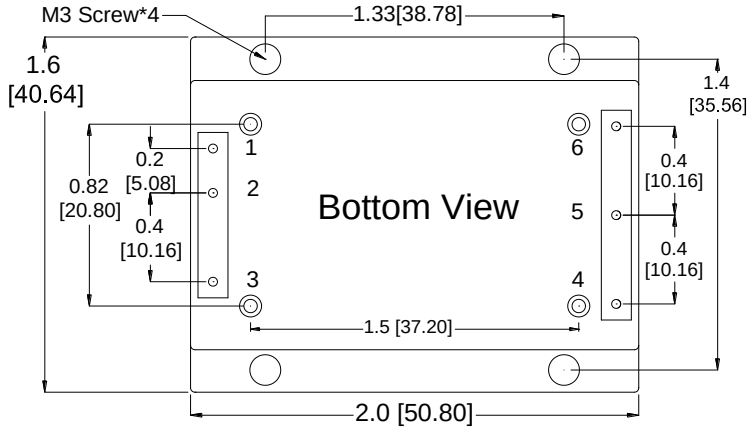


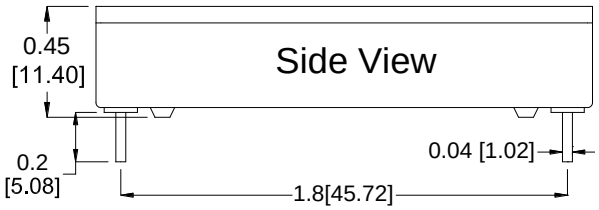
(Metal Case -Full metal jacket) "Six-Sided Continuous Shield"

Suffix –P(Heat Sink)



Suffix –R(Base Plate)





(Metal Case –Base Plate)

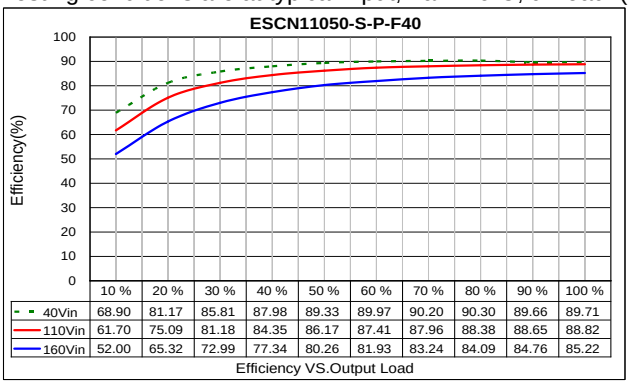
Pin Assignments:

Pin#	Single	Dual
1	+Vin	+Vin
2	-Vin	-Vin
3	Enable	Enable
4	Trim	-Vout
5	-Vout	Com
6	+Vout	+Vout

Note:
Pin Pitch tolerance: ±0.01 [0.25]
Pin Dimensions: .XX±0.02 [.X±0.5mm]
Pin Material: Copper Alloy
Pin Plating: Gold
Dimensions in inches [mm]
Tolerances: .XX±0.02 [.X±0.5mm]
.XXX±0.001 [.X±0.025mm]

Characteristic Curves

Testing conditions are at typical input, Ta=+25˚C,full load (horizontal mount) Unless otherwise indicated



	10 %	20 %	30 %	40 %	50 %	60 %	70 %	80 %	90 %	100 %
40Vin	68.90	81.17	85.81	87.98	89.33	89.97	90.20	90.30	89.66	89.71
110Vin	61.70	75.09	81.18	84.35	86.17	87.41	87.96	88.38	88.65	88.82
160Vin	52.00	65.32	72.99	77.34	80.26	81.93	83.24	84.09	84.76	85.22

Figure 1:Efficiency at Minimum, Nominal and Maximum Input voltages VS. output load.

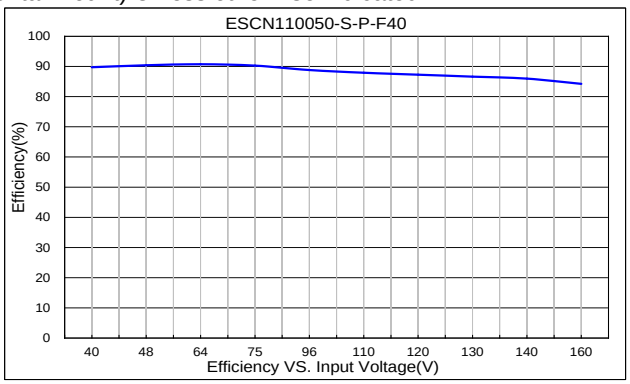


Figure 2: Efficiency VS. Input Voltages at 100% rated power

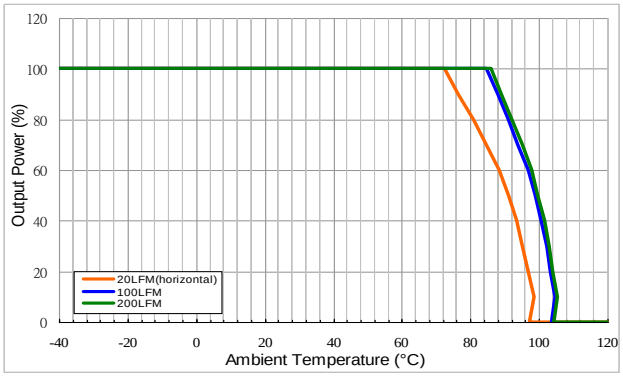


Figure 3: Ambient Temperature VS. Output Power Derating Curves

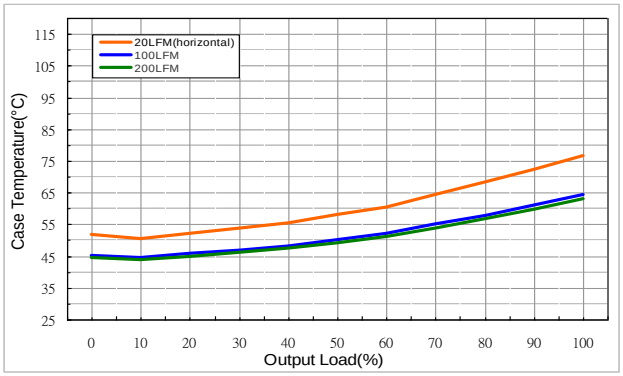


Figure 4: Case Temperature VS. Output rated Power

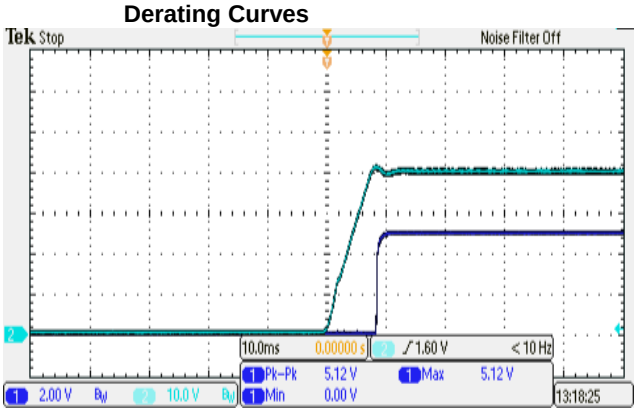


Figure 5: CH1 = Vout, CH2 = Nominal Input Typical Start-up waveform at Full load.

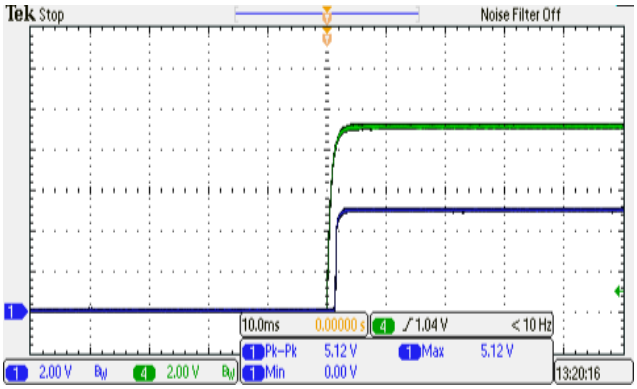


Figure 6: CH1 = Vout, CH4 = Enable Pin Typical Start-up waveform. Input voltage pre-applied

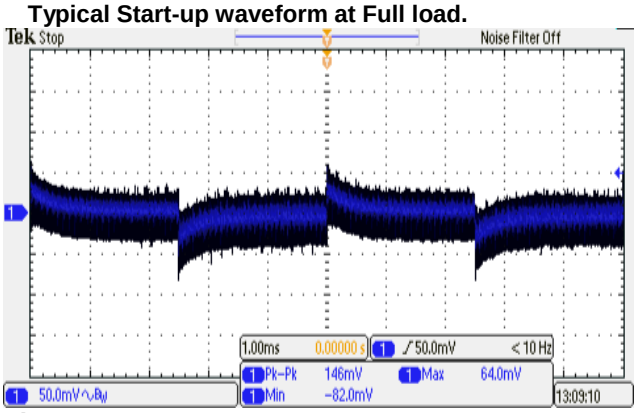


Figure 7: Transient Response at Output step load
(Vin: Typical ,50~75% of output current; ΔIo/Δt =0.01A/μS)

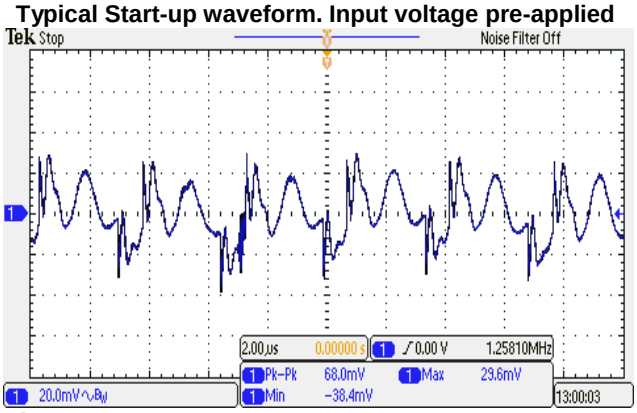
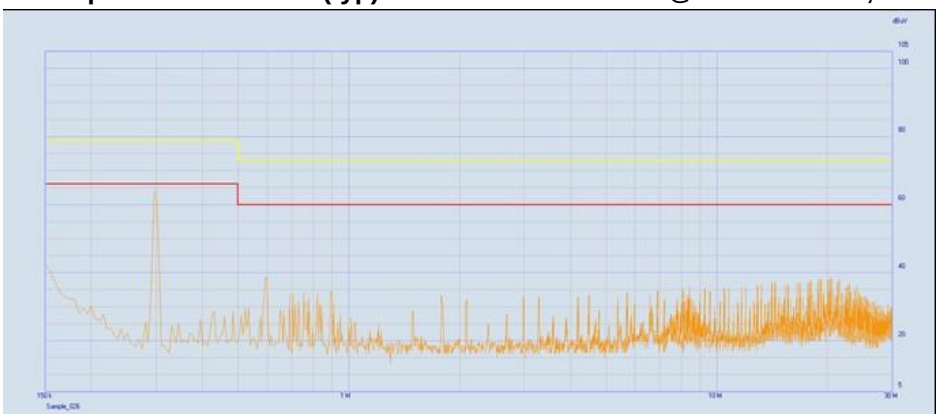


Figure 8: Output Voltage Ripple & Noise at full load.
(Vin: Typical, With Output Capacitor to add 1uF MLCC)

Conducted EMI **Input terminal value (typ) ESCN110050-S-P-F40@Vin = 110VDC, Iout =8A**



The fundamental switching frequency of the module is 260 kHz.

Trimming Output Voltage – for Single output models

Only the single output converters have a trim function. that allows users to adjust the output voltage from +10% to –10%, please refer to the trim table that follow for details. Adjustments to the output voltage can be used with a simple fixed resistor as shown in Figures 2 and 3. A single fixed resistor can increase or decrease the output voltage depending on its connection.

Note:

※ Trim adjustments higher than the specified range can have an adverse effect on the converter's performance and are not recommended.

※ If the trim function is not used, leave the trim pin open.

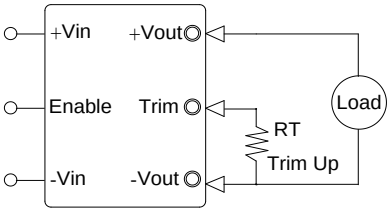


Figure 2. Trim Connections To increase Output Voltages Using Fixed Resistors

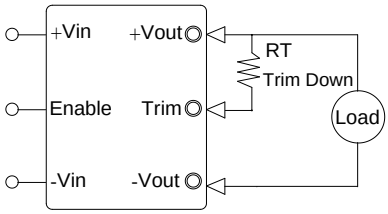


Figure 3. Trim Connections To Decrease Output Voltages Using Fixed Resistors

	Trim Up register value(KΩ) RT									
Vout	**1%**	**2%**	**3%**	**4%**	**5%**	**6%**	**7%**	**8%**	**9%**	**10%**
3.3	75	34	20.6	13.7	9.6	6.9	4.9	3.5	2.3	1.4
5	113	51	31.0	20.7	14.6	10.5	7.6	5.4	3.7	2.3
12	274	128	79.5	55.1	40.5	30.7	23.8	18.6	14.5	11.2
15	341	157	95.6	64.9	46.6	34.3	25.5	19.0	13.9	9.8
24	542	247	149	100	70.7	51.1	37.1	26.6	18.4	11.9

	Trim Down register value(KΩ) RT									
Vout	**-1%**	**-2%**	**-3%**	**-4%**	**-5%**	**-6%**	**-7%**	**-8%**	**-9%**	**-10%**
3.3	83	37	21.9	14.3	9.7	6.7	4.5	2.9	1.6	0.6
5	117	52	30.5	19.7	13.3	9.0	5.9	3.6	1.8	0.4
12	230	103	61.0	39.9	27.2	18.8	12.8	8.2	4.7	1.9
15	329	147	86.8	56.5	38.4	26.2	17.6	11.1	6.1	2.0
24	592	266	158	104	70.9	49.2	33.7	22.1	13.0	5.8

Enable Control Function

The primary-side, Enable Control function can be specified to operate with either positive or negative polarity. Positive-polarity devices are enabled when the enable pin is left open or is pulled high . See "Enable Function Input.

Positive-polarity devices are disabled when the enable pin is pulled low (under +1.0V with respect to –input). Negative-polarity devices are off when the enable pin is high/open and on when the enable pin is pulled low. See Figure 4.

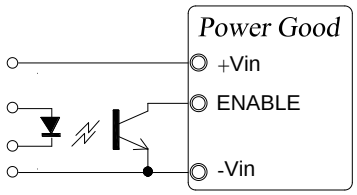


Figure 4. Driving the Enable Control pin

Output Ripple Noise

the two copper strips simulate real-world PCB impedances between the converter and its load. Scope measurements should be made using BNC connectors or The probe ground should be less than 1/2 inch and soldered directly to the fixture. All external capacitors should have appropriate voltage ratings and be located as close to the converter as possible. Temperature variations for all relevant parameters should be taken into consideration. The most effective combination of external I/O capacitors will be a function of line voltage and source impedance, as well as particular load and layout conditions. See Figure 5.

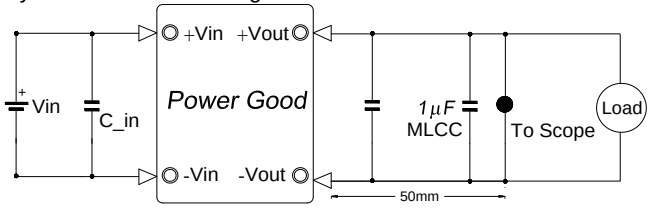


Figure 5. Measuring Output Ripple/Noise(20MHz bandwidth)